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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/799,193	03/12/2004	Hiromitsu Yamaguchi	1232-5326	1232-5326 8180	
27123	7590 07/14/2006		EXAMINER		
MORGAN & FINNEGAN, L.L.P. 3 WORLD FINANCIAL CENTER			GOLDBERG, BRIAN J		
	, NY 10281-2101		ART UNIT PAPER NUMBE		
			2861		
		•	DATE MAILED: 07/14/200	6	

Please find below and/or attached an Office communication concerning this application or proceeding.

			1 1 1
	Application No.	Applicant(s)	
	10/799,193	YAMAGUCHI ET AL.	
Office Action Summary	Examiner	Art Unit	
	Brian Goldberg	2861	
The MAILING DATE of this communication Period for Reply	appears on the cover sheet w	ith the correspondence address	
A SHORTENED STATUTORY PERIOD FOR RI WHICHEVER IS LONGER, FROM THE MAILIN - Extensions of time may be available under the provisions of 37 CF after SIX (6) MONTHS from the mailing date of this communicatio - If NO period for reply is specified above, the maximum statutory p - Failure to reply within the set or extended period for reply will, by s Any reply received by the Office later than three months after the earned patent term adjustment. See 37 CFR 1.704(b).	G DATE OF THIS COMMUNI FR 1.136(a). In no event, however, may a n. eriod will apply and will expire SIX (6) MON statute, cause the application to become Al	CATION. reply be timely filed NTHS from the mailing date of this communi BANDONED (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on 2	24 April 2006.		
2a)⊠ This action is FINAL . 2b)□	This action is non-final.		
3) Since this application is in condition for all	owance except for formal mat	ters, prosecution as to the meri	its is
closed in accordance with the practice und	der <i>Ex parte Quayle</i> , 1935 C.E). 11, 453 O.G. 213.	
Disposition of Claims			
4)⊠ Claim(s) <u>1-17</u> is/are pending in the applica	ition.		
4a) Of the above claim(s) is/are with	ndrawn from consideration.		
5) Claim(s) is/are allowed.			
6)⊠ Claim(s) <u>1-17</u> is/are rejected.			
7) Claim(s) is/are objected to.			
8) Claim(s) are subject to restriction a	nd/or election requirement.		
Application Papers			
9)☐ The specification is objected to by the Exa	miner.		
10)⊠ The drawing(s) filed on <u>12 March 2004</u> is/a	re: a)⊠ accepted or b)⊡ ob	jected to by the Examiner.	
Applicant may not request that any objection to	the drawing(s) be held in abeya	nce. See 37 CFR 1.85(a).	
Replacement drawing sheet(s) including the control of the control	•	• • •	- *
Priority under 35 U.S.C. § 119			
12)⊠ Acknowledgment is made of a claim for for a)⊠ All b)□ Some * c)□ None of:	eign priority under 35 U.S.C.	§ 119(a)-(d) or (f).	
1. Certified copies of the priority docur			
2. Certified copies of the priority docur		• • • • • • • • • • • • • • • • • • • •	
3. Copies of the certified copies of the	•	received in this National Stage	е
application from the International Bu * See the attached detailed Office action for a	• • • • • • • • • • • • • • • • • • • •	received	
See the attached detailed Office action for a	s list of the certified copies flot	received.	
Attachment(s)	,, —	(DTC 112)	
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) 		Summary (PTO-413) (s)/Mail Date	
3) Information Disclosure Statement(s) (PTO-1449 or PTO/S Paper No(s)/Mail Date		Informal Patent Application (PTO-152)	

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DETAILED ACTION

Claim Objections

- 1. Claims 2 and 6-15 are objected to because of the following informalities:
- 2. Claim 2 recites the limitation "the number of sets" in line 1 of the claim. There is insufficient antecedent basis for this limitation in the claim.
- 3. Claim 6 recites the limitation "the number of sets" in the third to last line of the claim. There is insufficient antecedent basis for these limitations in the claim.
- 4. Claim 11 recites the limitation "the number of sets" in the second to last line of the claim. There is insufficient antecedent basis for these limitations in the claim.

Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 6. Claims 1-17 are rejected under 35 U.S.C. 102(b) as being anticipated by Yano et al. (US 6352327).
- 7. Regarding claim 1, Yano et al. disclose "moving the print head (5 of Fig 2) and a print medium (1 of Fig 1) relative to each other in the scan direction (A and B of Fig 1) that crosses a direction of the columns of the print elements; and dividing the print elements into the plurality of drive blocks and activating the drive blocks of print elements on a time-division basis to form an image on the print medium (col 7 ln 54-66);

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wherein drive timings with which to activate the set of print elements aligned in the scan direction have the same time-division drive timing (col 6 ln 61-65)."

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- 8. Regarding claim 2, Yano et al. disclose "the number of sets of print elements aligned in the scan direction is equal to an integer times the number of drive blocks (col 9 ln 47-54)."
- 9. Regarding claim 3, Yano et al. disclose "the plurality of print elements in the print head are arranged in an entire widthwise printable range of the print medium (col 14 ln 32-38)."
- 10. Regarding claim 4, Yano et al. disclose "the plurality of print elements in the print head are ink jet print elements that can be activated to eject ink from nozzles (col 2 ln 66-67, col 6 ln 4-6)."
- 11. Regarding claim 5, Yano et al. disclose "the ink jet print elements have electrothermal transducers that generate energy for ejecting ink (col 13 ln 49-53)."
- 12. Regarding claim 6, Yano et al. disclose "a print head (5 of Fig 2) having a plurality of arrayed chips (5a-5d of Fig 1, col 14 ln 39-43), the chips each having a plurality of print elements arranged in columns (see Fig 3, N1, Ni, N64) and having a plurality of print elements divided in a number of time-division drive blocks (col 7 ln 54-66), the print elements are equal in number to an integer times the number of time-division drive blocks (col 9 ln 47-54); the print head and a print medium are moved relative to each other in a scan direction that crosses a direction of the columns of the print elements (A and B of Fig 1); the print elements of each of said drive blocks are activated in the drive blocks on a time-division basis to form an image on the print

medium (col 7 ln 54-66); at least two print elements in adjoining chips are aligned in the scan direction forming a set of print elements (see Fig 1, col 6 ln 52-61); and the number of sets or pairs of print elements in the adjoining chips aligned in the scan direction is equal to an integer times the number of time-division drive blocks (col 7 ln 62-66, col 9 ln 47-54)."

- 13. Regarding claim 7, Yano et al. disclose "the print elements aligned in the scan direction are allocated to the same drive block for activation (col 7 ln 54-66, col 6 ln 61-65)."
- 14. Regarding claim 8, Yano et al. disclose "the plurality of print elements in the print head are arranged in an entire widthwise printable range of the print medium (col 14 ln 32-38)."
- 15. Regarding claim 9, Yano et al. disclose "the plurality of print elements in the print head are ink jet print elements that can be activated to eject ink from nozzles (col 2 ln 66-67, col 6 ln 4-6)."
- 16. Regarding claim 10, Yano et al. disclose "the ink jet print elements have electrothermal transducers that generate energy for ejecting ink (col 13 ln 49-53)."
- 17. Regarding claim 11, Yano et al. disclose "a plurality of arrayed chips (5a-5d of Fig 1), the chips each having a plurality of print elements arranged in columns (see Fig 3, N1, Ni, N64) and having a plurality of print elements arranged in a number of time-division drive blocks (col 7 In 54-66), the print elements being equal in number to an integer times the number of time-division drive blocks (col 9 In 47-54); wherein the print head and a print medium are moved relative to each other in a scan direction that

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crosses a direction of the columns of the print elements (A and B of Fig 1); wherein the print elements of each of the drive blocks are activated in the drive blocks on a time-division basis to form an image on the print medium (col 7 ln 54-66); wherein at least two print elements in adjoining chips are aligned in the scan direction forming a set of print elements(see Fig 1, col 6 ln 52-61); wherein the number of sets of print elements in the adjoining chips aligned in the scan direction is equal to an integer times the number of drive blocks (col 7 ln 62-66, col 9 ln 47-54)."

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- 18. Regarding claim 12, Yano et al. disclose "the print elements aligned in the scan direction are allocated to the same drive block for activation (col 7 In 54-66, col 6 In 61-65)."
- 19. Regarding claim 13, Yano et al. disclose "the plurality of print elements in the print head are arranged in an entire widthwise printable range of the print medium (col 14 In 32-38)."
- 20. Regarding claim 14, Yano et al. disclose "the plurality of print elements in the print head are ink jet print elements that can be activated to eject ink from nozzles (col 2 ln 66-67, col 6 ln 4-6)."
- 21. Regarding claim 15, Yano et al. disclose "the ink jet print elements have electrothermal transducers that generate energy for ejecting ink (col 13 ln 49-53)."
- 22. Regarding claim 16, Yano et al. disclose "moving the print head (5 of Fig 2) and a print medium (1 of Fig 1) relative to each other in the scan direction (A and B of Fig 1) that crosses a direction of the columns of the print elements; activating the drive blocks of print elements on a time-division basis to form an image on the print medium (col 7 In

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54-66); and activating the set of print elements aligned in the scan direction at the same time-division drive timing (col 6 In 61-65)."

23. Regarding claim 17, Yano et al. disclose "a storage media readable by a computer and storing the program of claim 16 (20b,c of Fig 2, col 6 ln 30-34, col 15 ln 30-37)."

Response to Arguments

- 24. Applicant's arguments filed 4/24/06 have been fully considered but they are not persuasive.
- 25. In response to applicant's arguments, the recitation cited in claims 1 and 16 has not been given patentable weight because the recitation occurs in the preamble. A preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).
- 26. Furthermore, even if the limitations cited are given patentable weight, which is certainly true of claims 6 and 11, the cited Yano et al. reference discloses that the printhead may be an array of chips (col 14 ln 33-43) in substitution of the printhead described earlier in the specification which contains print elements aligned in a scan direction. Also, applicant's arguments do not specifically demonstrate how the language

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of the claims patentably distinguishes them from the references besides quoting portions of the claims.

Conclusion

27. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian Goldberg whose telephone number is 571-272-2728. The examiner can normally be reached on Monday through Friday, 9AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Vip Patel can be reached on 571-272-2458. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Brian Goldberg AU 2861

June 27, 2006

Vip Patel **Supervisory Examiner** AU 2861